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Size control of CuInSe₂ nanotube arrays *via* nanochannel-confined galvanic displacement[†]

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We report a facile and efficient method, nanochannel-confined galvanic displacement, to fabricate uniform and controllable CuInSe₂ nanotube arrays using the porous Au-coated anodic aluminium oxide template with a narrow ring-shaped Al foil surrounding on its outside edge. This method provides a new approach for fabricating nanotube arrays.

In the face of energy shortage and environmental pollution, solar cells are attractive candidates for the demand of clean and renewable power.^{1,2} In recent years, improved photoelectric properties such as high light-trapping, enhanced carrier collection have been reported in Si nanowire arrays,^{3,4} CdS nanopillar arrays,⁵ ZnO nanowire⁶ or nanotube arrays,7 TiO2 nanotube array dye-sensitized or solid-state heterojunction solar cells,^{8,9} and so on. As a vital material for the absorption layer of thin-film solar cells, CuInSe₂ (CIS) has been the center of much research due to its high optical absorption coefficient (10^5 cm^{-1}) , good radiation stability and desirable conversion efficiency.¹⁰⁻¹⁵ Some efforts based on solvothermal methods,^{16,17} vaporliquid-solid techniques,18 solution-liquid-solid,19 solid-state reactions²⁰ and other chemical approaches²¹ have been devoted to fabricate CIS nanowires with random distribution on the substrate. CIS nanowire arrays were firstly fabricated by porous anodic aluminium oxide (AAO) template-assisted pulse electrodeposition techniques, while only about 60% channels of AAO templates were filled with CIS nanowires.²² CIS nanotube arrays growing on the conducting glass substrate have also been realized via gradual ion-exchange and replacement reactions by using ZnO nanorod arrays as sacrificial templates, but with a cap layer covered on the top of nanotubes.23 However, it remains a great challenge to develop a facile and efficient method for controllable synthesis of CIS nanotube (or nanowire) arrays.

Recently, Meng et al.²⁴ reported an effective approach—redox reaction to synthesize metal nanowires (Au, Pt, Pd and so on) in

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AAO templates. Here, based on the nanochannel-confined galvanic displacement, we have successfully fabricated uniform, diameter and wall thickness-controllable ternary CIS nanotube arrays using the tunable porous Au-coated AAO template with a narrow ring-shaped Al foil surrounding on its outside edge (see the ESI⁺ for experimental details). The galvanic displacement provides the driving force to overcome the geometrical restrictions of inserting metallic ions into very deep nanochannels finally to the bottom porous Au film. Furthermore, the porous structure of the Au film and nanochannelconfined growth from the AAO template are responsible for tubular habits and make size-controllable (diameter, wall-thickness) synthesis of CIS nanotubes possible by tuning the pore size of the porous Au film and AAO template. Compared with conventional templateassisted electro-22,25 or electroless deposition techniques, 26 this method does not require electric power, especially complicated sensitization processes and complexing agents but with nearly 100% pore fill factor of AAO templates, and can achieve nearly pure chalcopyrite CIS nanotube arrays. Such pure chalcopyrite CIS nanotube arrays with adjustable size are expected to have important applications in high light-trapping and enhanced carrier collection solar cells. This nanochannel-confined galvanic displacement technique can also be employed to prepare other kinds of nanotube array materials.

The fabrication process is shown schematically in Fig. 1. When the porous Au-coated AAO template (with a narrow ring-shaped Al foil surrounding on its outside edge) was immersed in the aqueous solution (a mixture of CuCl₂, InCl₃ and SeO₂) under room temperature, the Al foil would initiate redox reactions on the top surface of the exposed Al foil and the bottom porous Au film to form CIS, because of the more negative redox potential of Al³⁺/Al⁰ ($E^0 = -1.67$ V vs SHE (standard hydrogen electrode)) than Cu²⁺/Cu⁰ ($E^0 = 0.337$ V vs SHE), In³⁺/In⁰($E^0 = -0.342$ V vs SHE) and SeO₃²⁻/Se⁰ ($E^0 = -0.366$ V vs SHE).²⁷ Since the free energy of CIS formation has been shown to be about 10–80 kJ more stable than the codeposition of the mixture of binaries Cu₂Se + In₂Se₃,²⁸ the possible total galvanic displacement reaction to the formation of CIS can be represented as follows:

 $3Cu^{2+}(aq.) + 3In^{3+}(aq.) + 6SeO_3^{2-}(aq.) + 13Al^{0}(s) + 36H^{+}(aq.)$ $\rightarrow 3CuInSe_2(s) + 13Al^{3+}(aq.) + 18H_2O$ (1)

The galvanic displacement reactions for the top galvanic cell occur on the top surface of the exposed Al foil, where some locations of the

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Fig. 1 Schematic diagram of the fabrication process for CIS nanotube arrays *via* nanochannel-confined galvanic displacement. (a) The two-way AAO template with a narrow ring-shaped AI foil surrounding on its outside edge. (b) The porous Au film sputtered on one surface of the AAO template for the formation of galvanic contact to the AI foil. (c) Galvanic displacement reactions for both the bottom and top galvanic cells. (d) Parallel growth of CIS on the porous Au film and along pore walls of the AAO template, producing CIS nanotube arrays.

Al foil act as anodes and are oxidized into Al³⁺, then at some other locations of the Al foil (as cathodes), metallic ions in the solution can gain electrons (come from the anode and through the Al foil itself) to form CIS on the cathodes and followed by diffusing away from the Al foil and into channels of the AAO template because of the concentration gradient. Meanwhile, metallic ions on the porous Au film can also gain electrons and be reduced to form CIS from the bottom galvanic cell where the Al foil serves as the anode and the porous Au film (with good electrical contact with the Al foil) acts as the cathode. In this bottom galvanic cell, both electrons (generated from the anode-Al foil) and metallic ions (in the solution or channels) move to the cathode, resulting in continuous growth of CIS upward the channels of the AAO template. With increasing duration of immersion, the diffusing CIS (from the top galvanic cell) reaches the tips of CIS nanotubes (from the bottom galvanic cell) and combines with them, leading to the formation of longer CIS nanotube arrays confined in channels of the AAO template.

As CIS nanotube arrays grow on the porous Au film and are confined in channels of the AAO template, their outer diameters and wall thicknesses are completely dependent on the pore size of AAO templates and porous Au films. Fig. 2(b) shows a FE-SEM image of CIS nanotube arrays on the porous Au film after partial removal of the AAO template. The lengths of CIS nanotube arrays are about 2.4 µm-2.85 µm with immersing time of 1 h. The average outer and inner diameters of nanotubes are 70 nm and 30 nm (wall thickness of 20 nm), in agreement with the pore diameter of the AAO template (ESI, Fig. S1(a)[†]) and the porous Au film (Fig. 2(a)). The EDS spectrum of Fig. 2(c) indicates that the deposited samples are nearstoichoimetric CuInSe₂ nanotube arrays with the Cu, In and Se atomic ratio of 4:5:8.9. The broadened diffraction peaks in the XRD pattern (see ESI, Fig. S1(b)-b⁺) of the as-deposited sample can be indexed to chalcopyrite CIS and porous Au films. TEM analyses (Fig. 2(d)) also confirm the formation of chalcopyrite CIS nanotube arrays composed of nanocrystals with an average grain size of 10 nm at room temperature. The characteristic A1 modes (Raman peak of 177.3 cm⁻¹) of CIS were also observed on the micro-Raman spectra (see ESI, Fig. S2(a)[†]) for different locations of the as-deposited sample, which further demonstrates the formation of chalcopyrite CIS phase.^{17,29} A very weak peak at 261 cm⁻¹ on the micro-Raman spectrum for the location c may be ascribed to the presence of minor binary Cu_xSe phase,²⁹ indicating that the as-deposited sample is not completely uniform. After the annealing process, the average grain size in CIS nanotubes increases to 20 nm and the crystallinity of nanotubes is improved according to the XRD pattern (see ESI, Fig. S1(b)-c[†]), HRTEM image, SAED analysis (see ESI, Fig. S1(c)[†])

and Raman spectrum (see ESI, Fig. S2(b)†). In addition, the influence of the distance from the Al ring on the lengths of CIS nanotubes has also been investigated (see ESI, Fig. S3†). It is found that the closer the location is to the ring-shaped Al foil, the longer the CIS nanotubes would be achieved in the corresponding positions, which may be caused by the shorter diffusion length of CIS from the top galvanic cell. It should be noted that the porous structure of Au films is critical to tubular habits, if the sputtered Au film is thick enough to almost completely cover pores of the AAO template, CIS nanowire arrays would be achieved (see ESI, Fig. S4†).

The fabrication process of nanotube arrays allows the outer diameter and wall thickness of nanotubes to be tuned systematically by controlling the pore diameter of AAO templates and porous Au films. Based on our previous high-field technique,³⁰ we can achieve desirable AAO templates with large diameters, which can help us adjust outer and inner diameters (or wall thickness) of nanotubes over a large range. As an example, Fig. 3(a) and (b) show FE-SEM images of CIS nanotube arrays uniformly embedded in AAO



Fig. 2 FE-SEM images of (a) the porous Au film sputtered on the surface of the AAO template with average pore diameter of 30 nm and (b) CIS nanotube arrays (the inserts are enlarged images). (c) The EDS spectrum of CIS nanotube arrays embedded in the AAO template. (d) TEM image, HRTEM image and SAED pattern of the as-deposited CIS nanotube.



Fig. 3 FE-SEM images of CIS nanotube arrays with the same outer diameter of 180 nm and length about $2.4 \,\mu\text{m}-2.85 \,\mu\text{m}$ (with immersing time of 1 h), but different wall thicknesses of (a) 20 nm, (b) 30 nm and (c) 7 nm.

templates (the inserts are enlarged images) with the average outer diameter of 180 nm and different inner diameters about 140 nm (wall thickness of 20 nm) and 120 nm (wall thickness about 30 nm), which are consistent with the pore size of the AAO template (see ESI, Fig. S5(a)†) and porous Au films (see ESI, Fig. S5(b) and (c)†), respectively. Based on this method, we can also realize the fabrication of ultrathin CIS nanotube arrays (Fig. 3(c)) with nearly porous-wall (average wall thickness of 7 nm) by controlling the pore diameter of the porous Au film close to the pore size of the AAO template (see ESI, Fig. S5(d)†), which may have potential applications in enhanced carrier collection efficiency because of minimal radial transport pathways for photo-generated carriers.^{7,8}

In summary, we develop a facile method—nanochannel-confined galvanic displacement—for large-scale fabrication of uniform and size-controllable CIS nanotube arrays. The CIS nanotube arrays fabricated by this method are almost pure chalcopyrite CIS nanocrystals at room temperature, and with nearly 100% pore fill factor in the AAO template. The outer and inner diameters (or wall thickness) of CIS nanotubes can be tuned *via* modulating the pore diameter of AAO templates and porous Au films. Such uniform and size-tunable CIS nanotube arrays can support the design of high-efficiency nanostructured solar cells. Using this method, other nanotube array materials (with higher redox potential than Al) can also be realized (see ESI, Fig. S6 for Co and Fig. S7† for CuSe nanotube arrays), which may open a new branch of nanofabrication.

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